FIG.1 Related Art

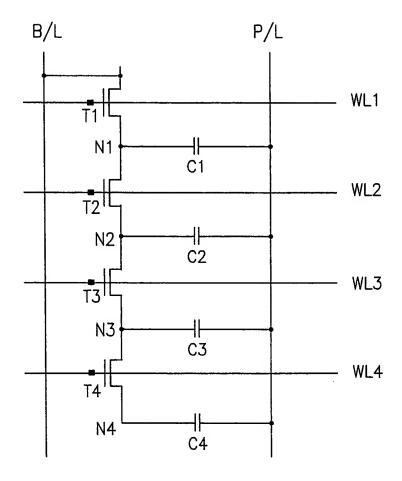


FIG.2 Related Art

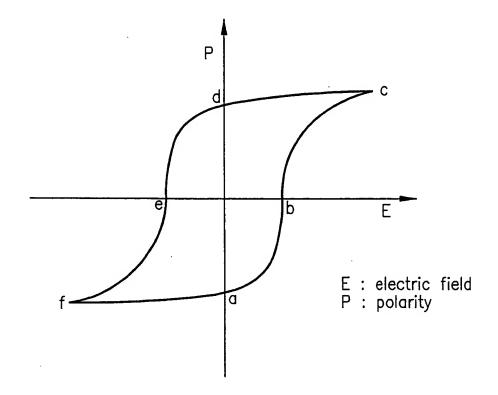
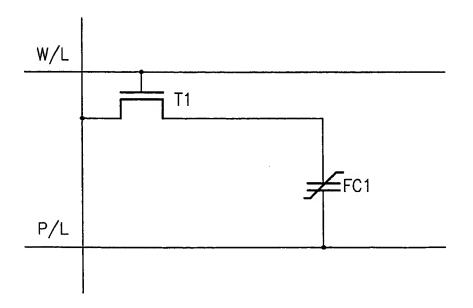
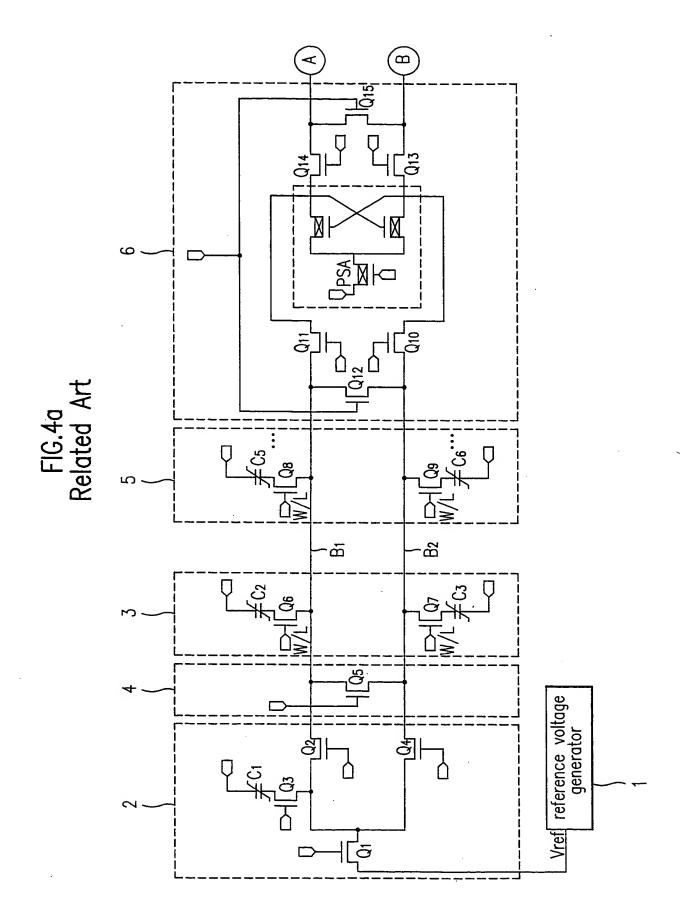


FIG.3 Related Art





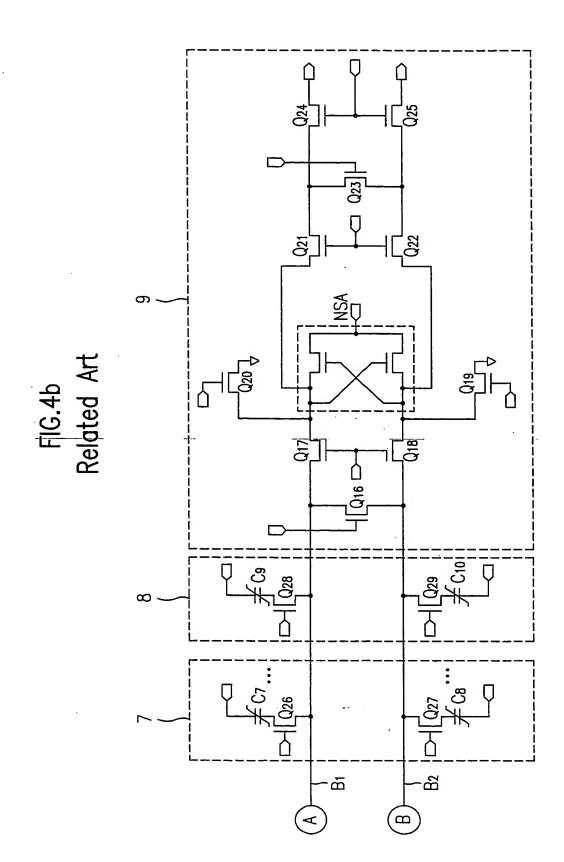


FIG.5 Related Art

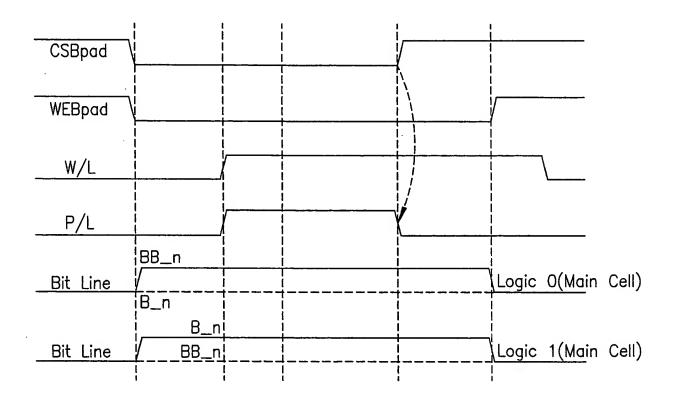


FIG.6 Related Art

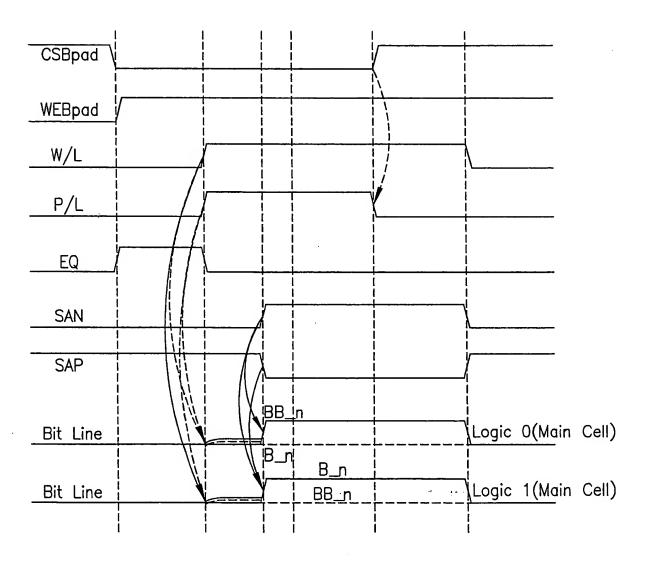
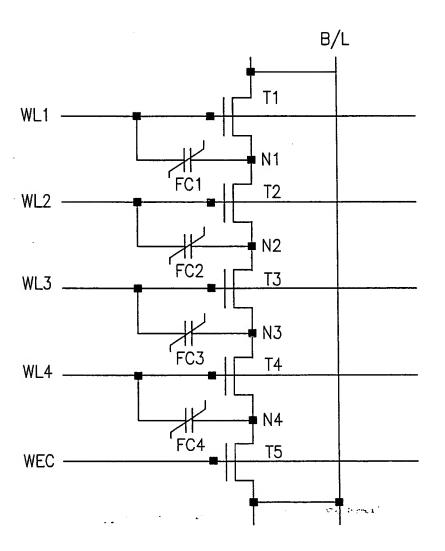


FIG.7a



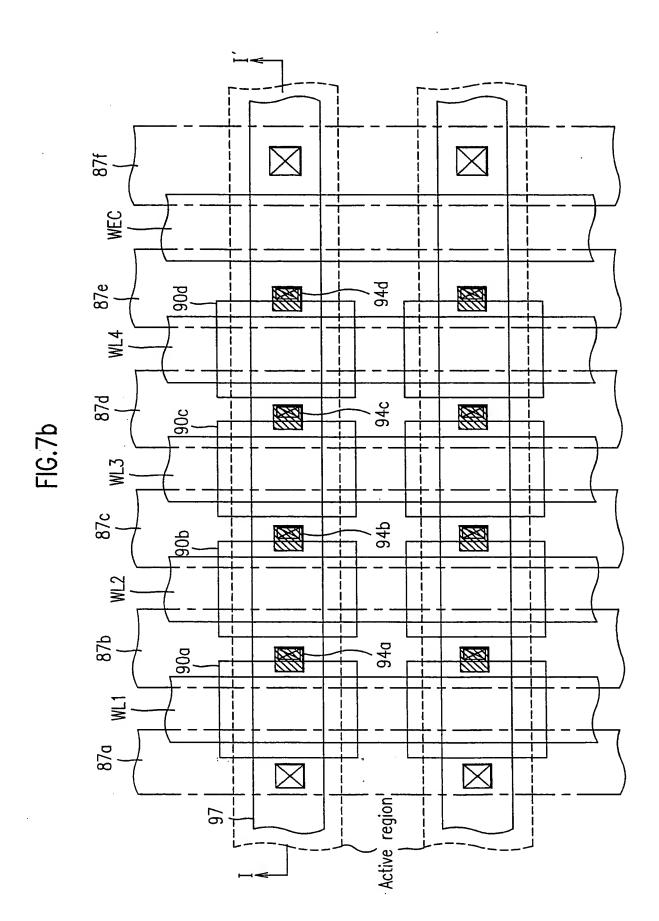


FIG.7c

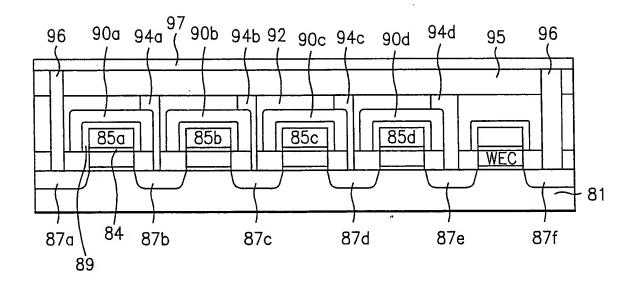


FIG.8a

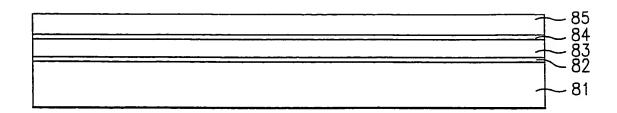


FIG.8b

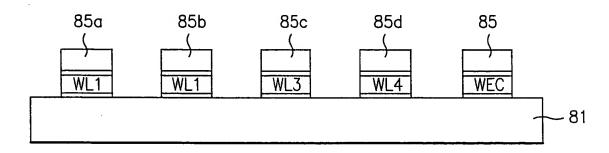


FIG.8c

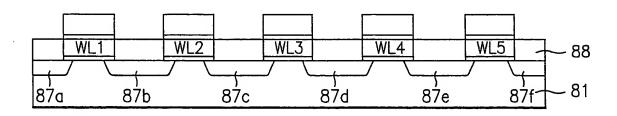


FIG.8d

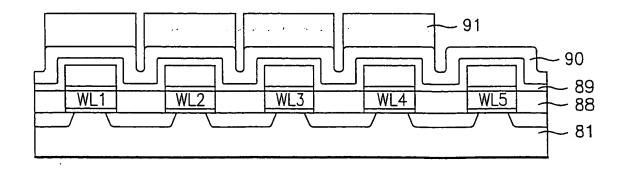


FIG.8e

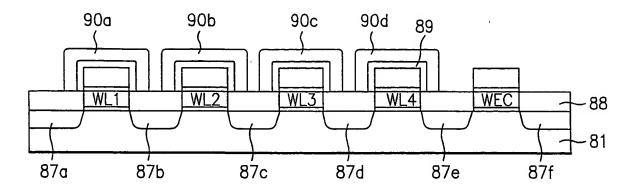


FIG.8f

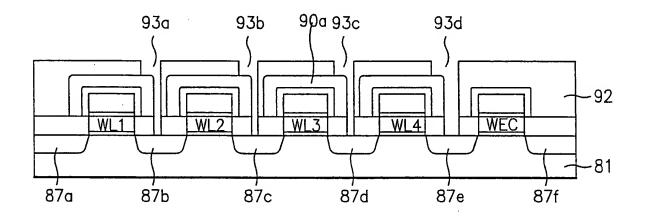


FIG.8g

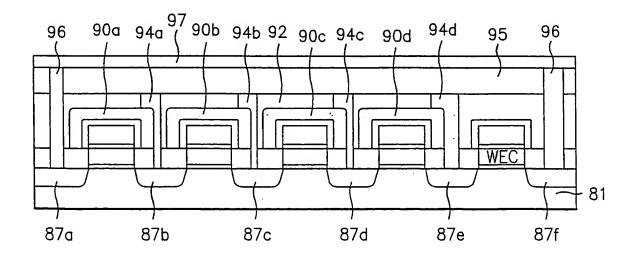


FIG.9a

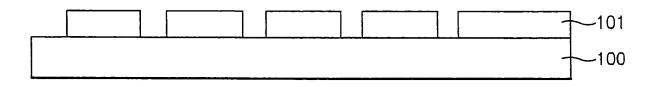


FIG.9b

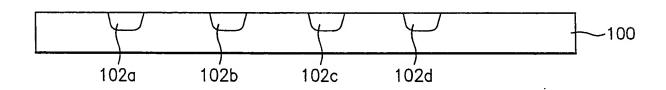


FIG.9c

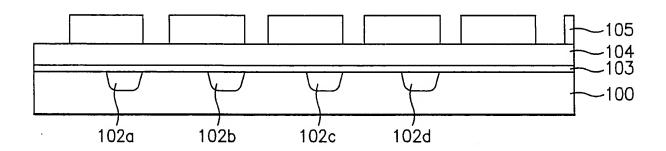


FIG.9d

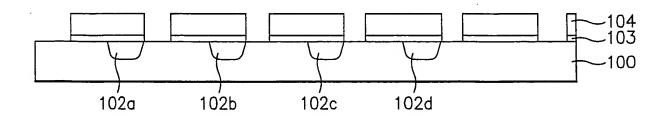


FIG.9e

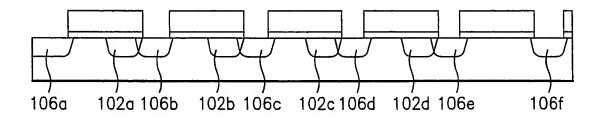


FIG.9f

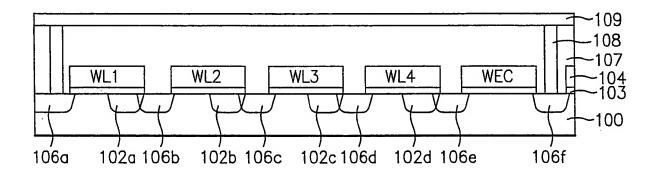


FIG.9g

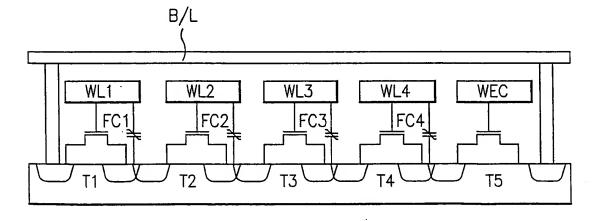


FIG.10

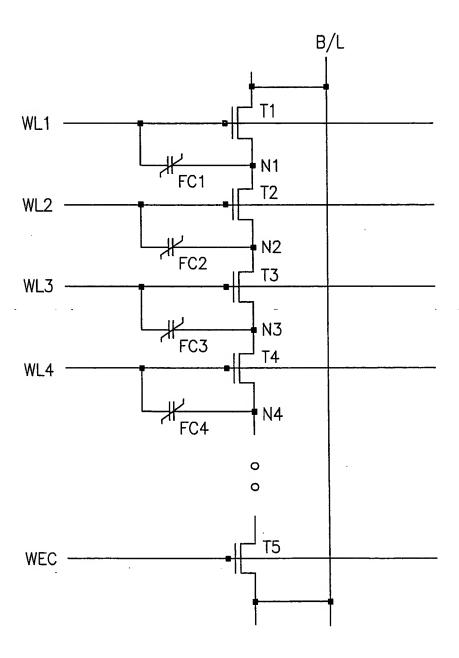
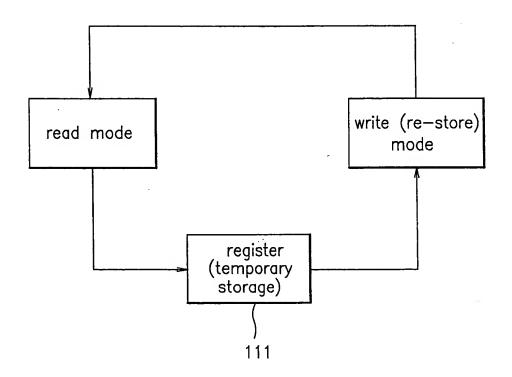


FIG.11



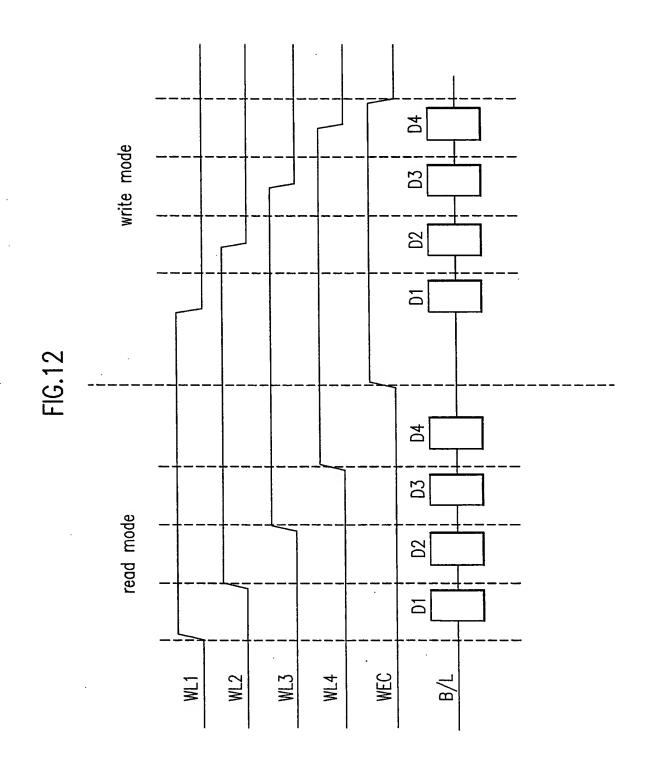


FIG.13a

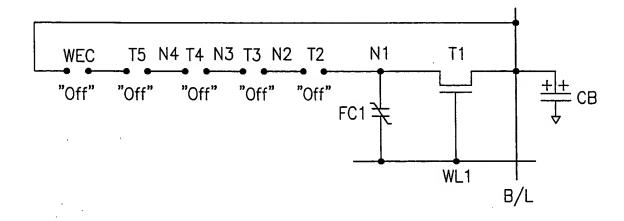


FIG.13b

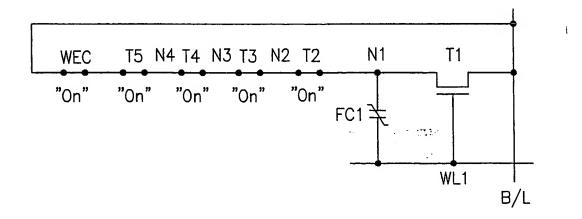


FIG.14a

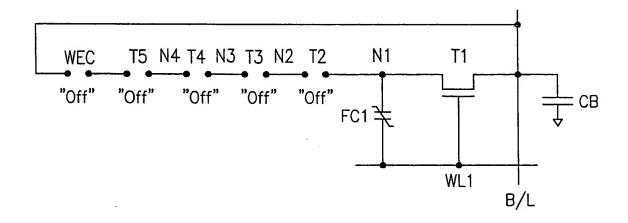


FIG.14b

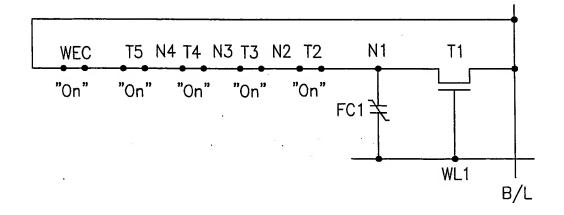


FIG.15a

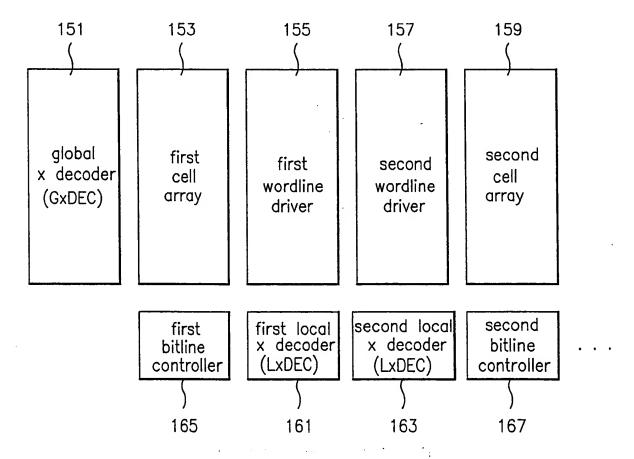


FIG.15b

global x decoder (GxDEC)	first cell array	first wordline driver	second wordline driver	second cell array
	first bitline controller	first local x decoder (LxDEC)	second local x decoder (LxDEC)	second bitline controller

FIG.16

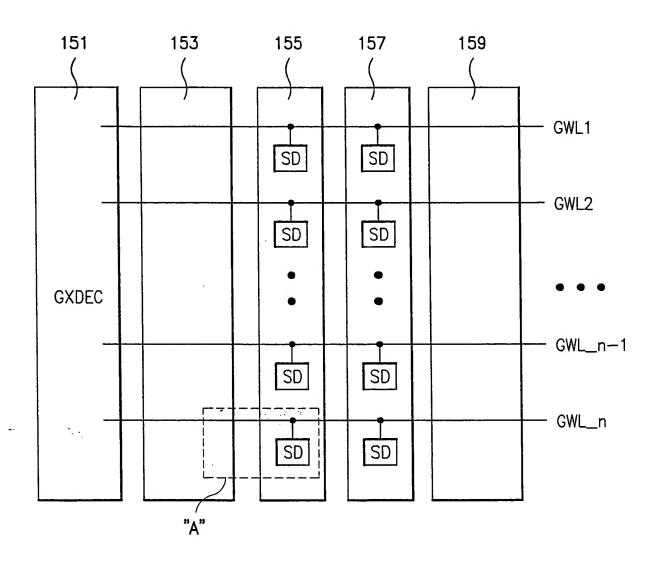


FIG.17

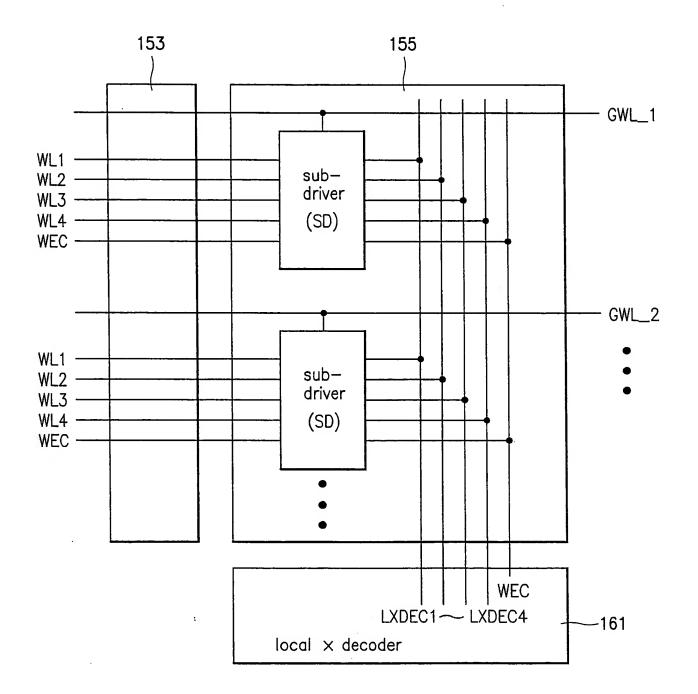


FIG.18

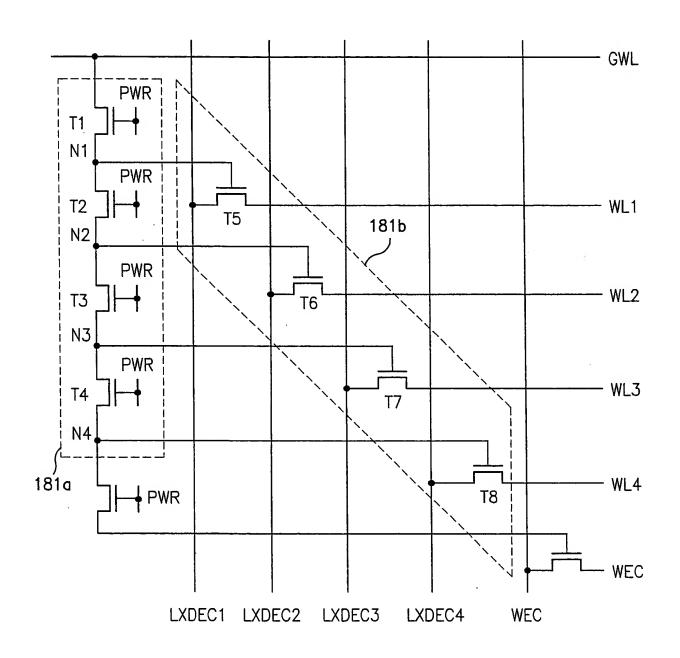
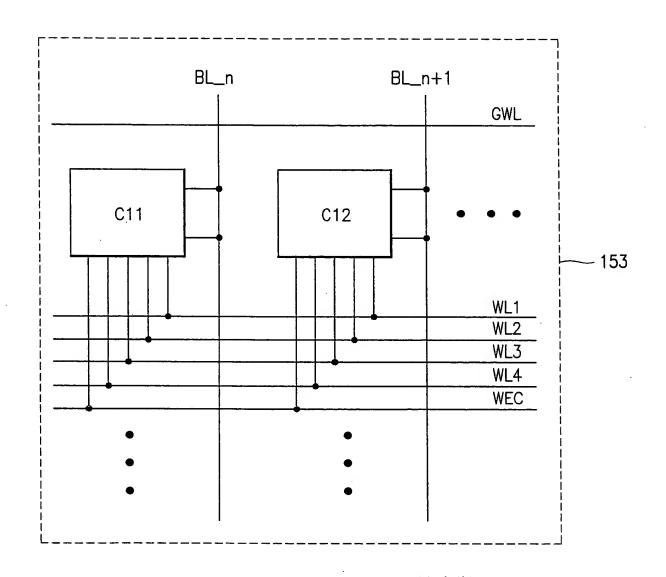
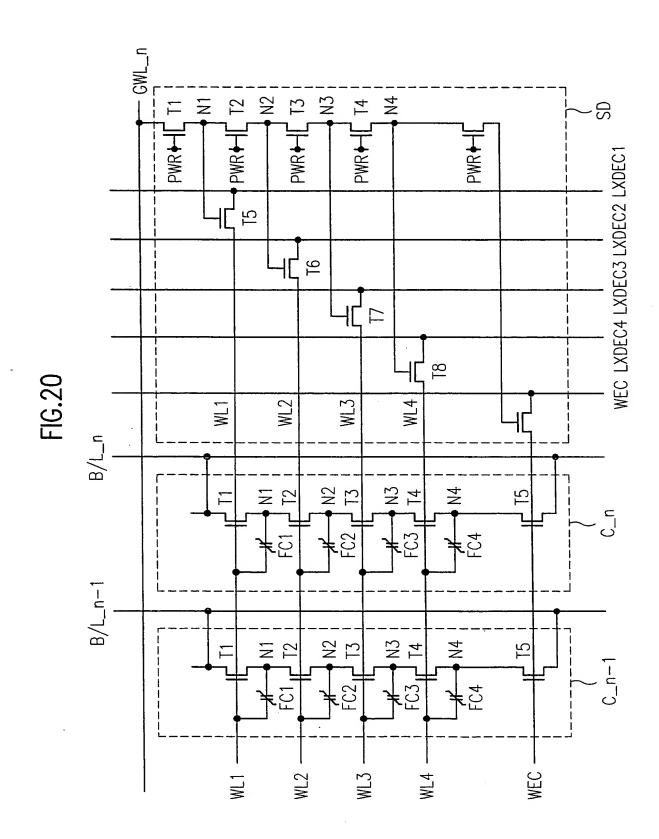


FIG.19





. 30

FIG.21

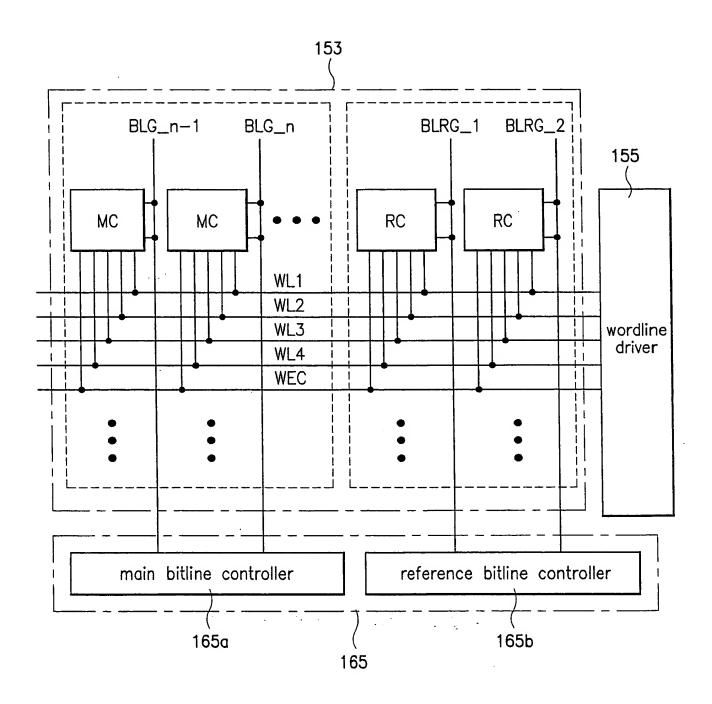


FIG.22

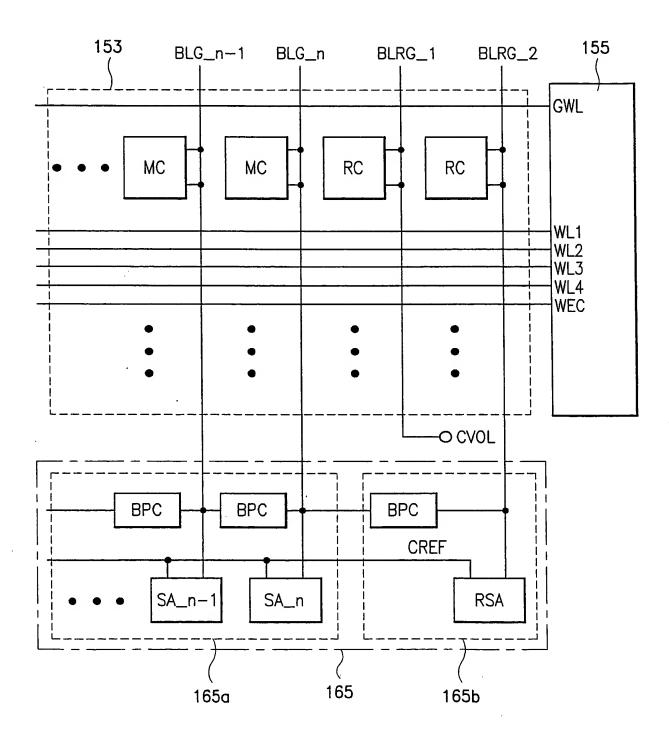
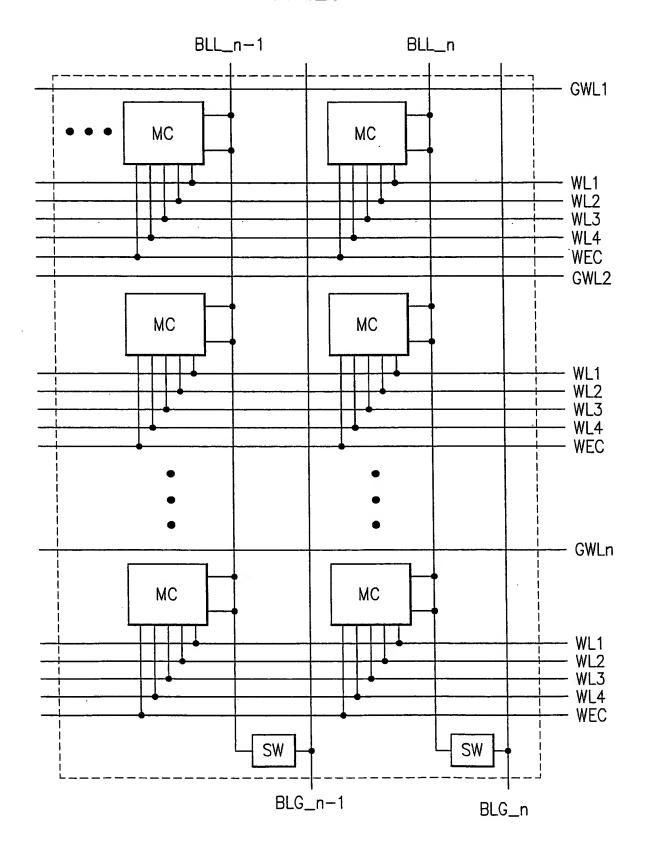


FIG.23



3

2

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151

BPC

FIG.25

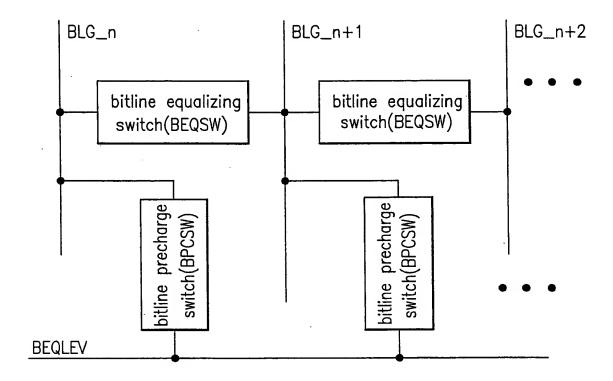


FIG.26

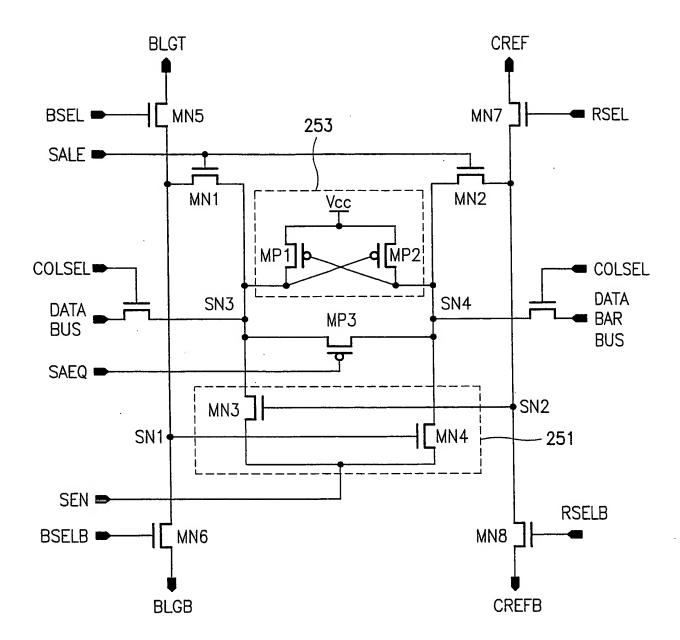


FIG.27

